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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/712,452	11/12/2003	Chang-Woo Ha	4611-030	9296
22440 75	90 04/14/2005		EXAMINER	
	ACKMAN & REISN	LAM, TUAN THIEU		
270 MADISON AVENUE 8TH FLOOR			ART UNIT	PAPER NUMBER
NEW YORK, 1	NY 100160601		2816	

DATE MAILED: 04/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

				<i>Cli</i>
		Application No.	Applicant(s)	
		10/712,452	HA ET AL.	
	Office Action Summary	Examiner	Art Unit	
		Tuan T. Lam	2816	
Period fo	The MAILING DATE of this communication ap or Reply	pears on the cover sheet with	the correspondence addre	ss
A SH THE - Exte after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLEMAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1. SIX (6) MONTHS from the mailing date of this communication. It is period for reply specified above is less than thirty (30) days, a repropersion of the propersion	136(a). In no event, however, may a reply ly within the statutory minimum of thirty (3 will apply and will expire SIX (6) MONTH! e, cause the application to become ABAN	y be timely filed 30) days will be considered timely. S from the mailing date of this common DONED (35 U.S.C. § 133).	unication.
Status				
	Responsive to communication(s) filed on 24. This action is FINAL.  2b) This Since this application is in condition for allowed closed in accordance with the practice under	s action is non-final. ance except for formal matters	•	erits is
Disposit	ion of Claims			
5)□ 6)⊠ 7)□	Claim(s) 1-30 is/are pending in the application 4a) Of the above claim(s) is/are withdraw Claim(s) is/are allowed.  Claim(s) 1-30 is/are rejected.  Claim(s) is/are objected to.  Claim(s) are subject to restriction and/or contents are subject to restriction.	awn from consideration.		
Applicat	ion Papers			
10)🛚	The specification is objected to by the Examin The drawing(s) filed on 12 November 2003 is Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Examin	are: a) ☐ accepted or b) ☑ o drawing(s) be held in abeyance ction is required if the drawing(s)	. See 37 CFR 1.85(a). is objected to. See 37 CFR 1	.121(d).
Priority (	under 35 U.S.C. § 119			
a)	Acknowledgment is made of a claim for foreign All b) Some * c) None of:  1. Certified copies of the priority documen 2. Certified copies of the priority documen 3. Copies of the certified copies of the priority application from the International Burea See the attached detailed Office action for a list	ts have been received. ts have been received in App prity documents have been re tu (PCT Rule 17.2(a)).	lication No ceived in this National Sta	ge
2) 🔲 Notic 3) 🔲 Infori	t(s) se of References Cited (PTO-892) se of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08 r No(s)/Mail Date		nmary (PTO-413) fail Date mal Patent Application (PTO-152	2)

#### **DETAILED ACTION**

This is a response to the amendment filed 1/24/2005. Claims 1-30 are pending. Applicant's arguments with respect to claims 1-30 have been considered but are moot in view of the new ground(s) of rejection.

### **Drawings**

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the limitation of "a feedback resistor coupled between the input of said amplifier and the output of one of said amplifier and emitter follower must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will

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be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

## Claim Rejections - 35 USC § 112

- 1. The following is a quotation of the second paragraph of 35 U.S.C. 112:
  - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 2. Claims 1-18, 21, 23, 24, 27, 29 and 30 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 1, the recitation of a feedback resistor coupled between the input of said amplifier and the output of one of said amplifier and emitter follower is misdescriptive. Figures 2, 5, 6 and 10 all show feedback resistor coupled between the output of the output buffer G3 and the input of the input amplifier. Correction is required.

In claims 21, 23-24, 27, 29 and 30, the recitation of "the first and second currents" lacks proper antecedent basis.

Claims 2-18 are indefinite because of the technical deficiencies of claim 1.

## Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this

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subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

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Claims 19, 25 and 28 are rejected under 35 U.S.C. 102(e) as being anticipated by 4. Nishizono (USP 6,476,954). Figure 12 of Nishizono shows a current to voltage transforming circuit comprising a photo detector (PD) for generating a photo curent (Ipd) in response to a photo signal inputted into the photo detector, a trans-impedance amplifier (Tr3, Rc1, Tr7) having an amplifier (Tr3, Rc1) amplifying the photo current (I1) received fro the photo detector to generate an output voltage, an output means (Tr7, 27b) for outputting said output voltage, the amplifier generating a current (current flows through resistors Rc1), a curent detecting limiter (Tr5, Tr4, Rc2, Tr8) generating a limiter current (Ipd2) in response to said current generated by the amplifier (the current flows to the base of the transistors Tr8) so that the trans-impedance amplifier is prevented from saturation in the presence of the excessive photo signal (the on/off of the transistor Tr5 maintaining the Idp1 constant through preventing the transistor Tr3 of the amplifier from saturation), a feedback resistor (Rf1) coupled between the input of the transimpedance amplifier buffer ant output the output means so that the photo current is converted into the output voltage (Vout1) as called for in claims 19 and 25.

Regarding claim 28, the limiter current is inputted to the reference voltage (ground) via transistor Tr5.

## Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are

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such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 20, 22 and 26 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nishizono (USP 6,476954). ). Figure 12 of Nishizono shows a current to voltage transforming circuit comprising a photo detector (PD) for generating a photo curent (Ipd) in response to a photo signal inputted into the photo detector, a trans-impedance amplifier (Tr3, Rc1, Tr7) having an amplifier (Tr3, Rc1) amplifying the photo current (I1) received fro the photo detector to generate an output voltage, an output means (emitter follower comprises transistor Tr7 and a current 27b) for outputting said output voltage, the amplifier generating a current (current flows through resistors Rc1), a curent detecting limiter (Tr5, Tr4, Rc2, Tr8) generating a limiter current (Ipd2) in response to said current generated by the amplifier (the current flows to the base of the transistors Tr8) so that the trans-impedance amplifier is prevented from saturation in the presence of the excessive photo signal (the on/off of the transistor Tr5 maintaining the Idp1 constant through preventing the transistor Tr3 of the amplifier from saturation), a feedback resistor (Rf1) coupled between the input of the trans-impedance amplifier buffer ant output the output means so that the photo current is converted into the output voltage (Vout1).

Nishizono does not show a buffer coupled to the emitter follower (Tr7, 27b) as called for in claim 20. However, it is well known in the art to employ a buffer circuit to amplify and preserve the integrity of a signal. Therefore, it would have been obvious to a person skilled in the art at the time of the invention was made to include a buffer for amplifying and preserving the output voltage Vout1 of Nishizono.

Regarding claim 22, the limiter current is inputted to the reference voltage (ground) via transistor Tr5.

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7. Claims 1-18 would be allowable if rewritten or amended to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action.

#### Conclusion

- 8. Claims 21, 23-24, 27 and 29-30 are indefinite and no prior art can be applied at this time.
- 9. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Lam whose telephone number is 571-272-1744. The examiner can normally be reached on Monday to Friday (7:30 am to 6:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P. CALLAHAN can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan T. Lam

Primary Examiner
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4/11/2005